

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>1550</b>	<b>(361/767-771,803).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>189</b>	<b>1 and (interposer\$1 adapter\$1 translation)</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>28</b>	<b>2 and (lga (land adj grid adj array\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L5</b>	<b>0</b>	<b>4 and (processor\$1 micro\$1processor\$1 cpu\$1 micro\$1computer\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L6</b>	<b>0</b>	<b>257/686,693,697,698,700, 701,723,724,737,738, 778</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L7</b>	<b>7523</b>	<b>(257/686,693,697,698,700, 701,723,724,737,738, 778).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L8</b>	<b>723</b>	<b>7 and (interposer\$1 adapter\$1 translation)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>8</b>	<b>L9</b>	<b>161</b>	<b>2 not 3</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L10</b>	<b>669</b>	<b>8 not 2</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L11</b>	<b>301</b>	<b>10 and (test\$3 de\$1bug\$4 bug\$4)</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L12</b>	<b>368</b>	<b>10 not 11</b>	<b>USP AT; US-P GPU B</b>
<b>12</b>	<b>L13</b>	<b>312</b>	<b>1 and stack\$4</b>	<b>USP AT; US-P GPU B</b>
<b>13</b>	<b>L14</b>	<b>2191</b>	<b>7 and stack\$4</b>	<b>USP AT; US-P GPU B</b>
<b>14</b>	<b>L15</b>	<b>247</b>	<b>13 not (2 10)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>15</b>	<b>L16</b>	<b>759</b>	<b>14 and (test\$3 de\$1bug\$4 bug\$4)</b>	<b>USP AT; US-P GPU B</b>
<b>16</b>	<b>L17</b>	<b>592</b>	<b>16 not (2 10 15)</b>	<b>USP AT; US-P GPU B</b>
<b>17</b>	<b>L18</b>	<b>34</b>	<b>17 and (lga (land adj grid adj array\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>18</b>	<b>L19</b>	<b>739</b>	<b>257/48.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>19</b>	<b>L20</b>	<b>29</b>	<b>19 and (interposer\$1 adapter\$1 translation)</b>	<b>USP AT; US-P GPU B</b>